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(71)Applicant : **CASIO COMPUT CO LTD**

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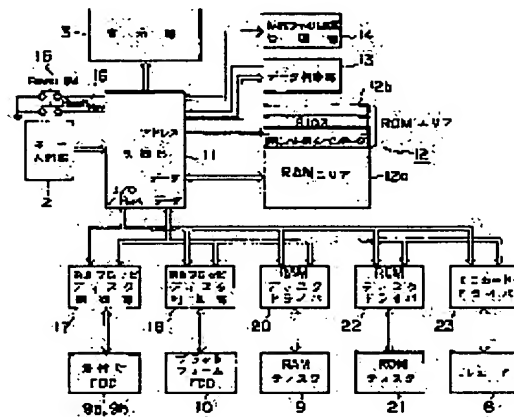
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(54) DATA PROCESSOR

(57)Abstract:

PURPOSE: To effectively use a storage capacity more than one M bytes of an IC card by reading prescribed format data on a previously stored hard disk at the time of data-accessing the IC card.

CONSTITUTION: When the switch 15 of a processor is turned on, a control part executes an initializing processing, and executes the format check processing of the IC card 8 and it is recognized that FD access by BIOS held in a ROM area 12b is possible because the processor is constructed in the same way as FD. Respective external file devices are sequentially accessed by previously set access priority based on connection management data which an external file device management part 14 manages. The IC card 8 is access-processed as the hard disk by permitting the control part 11 to read previously held pseudo partition data in the ROM area 12b at the time of accessing.



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